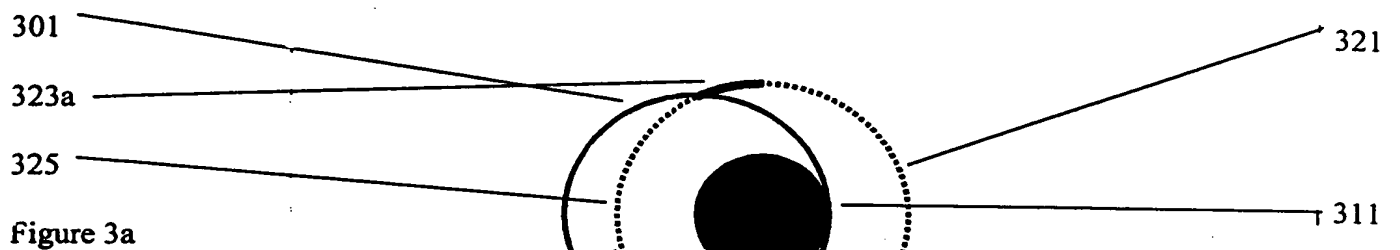


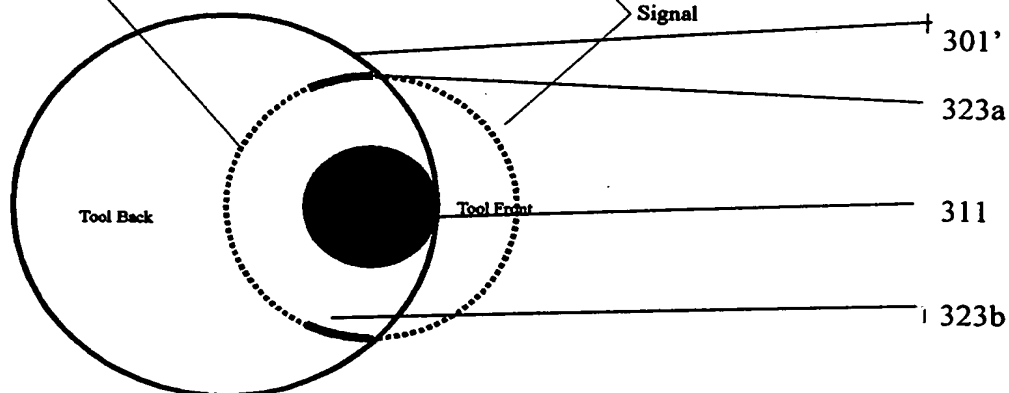
FIG. 2

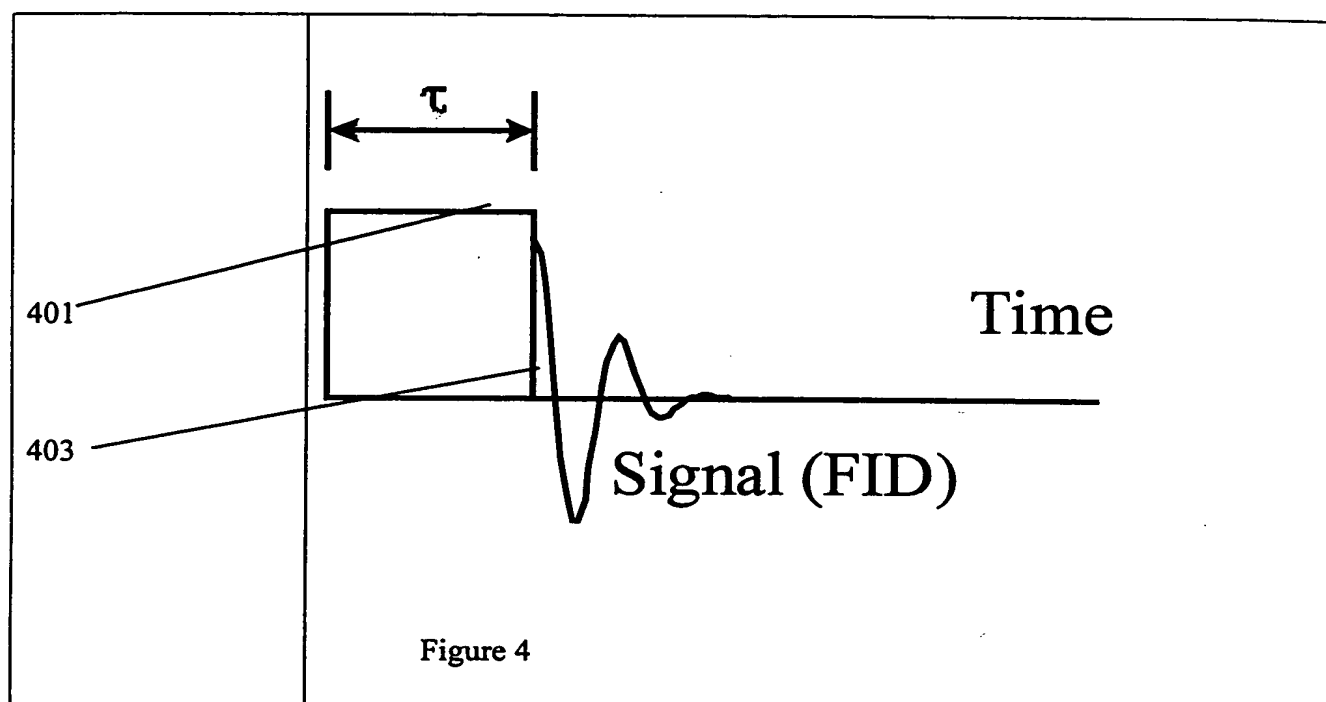


Hardware
Compensated

Desired
Signal

Figure 3b





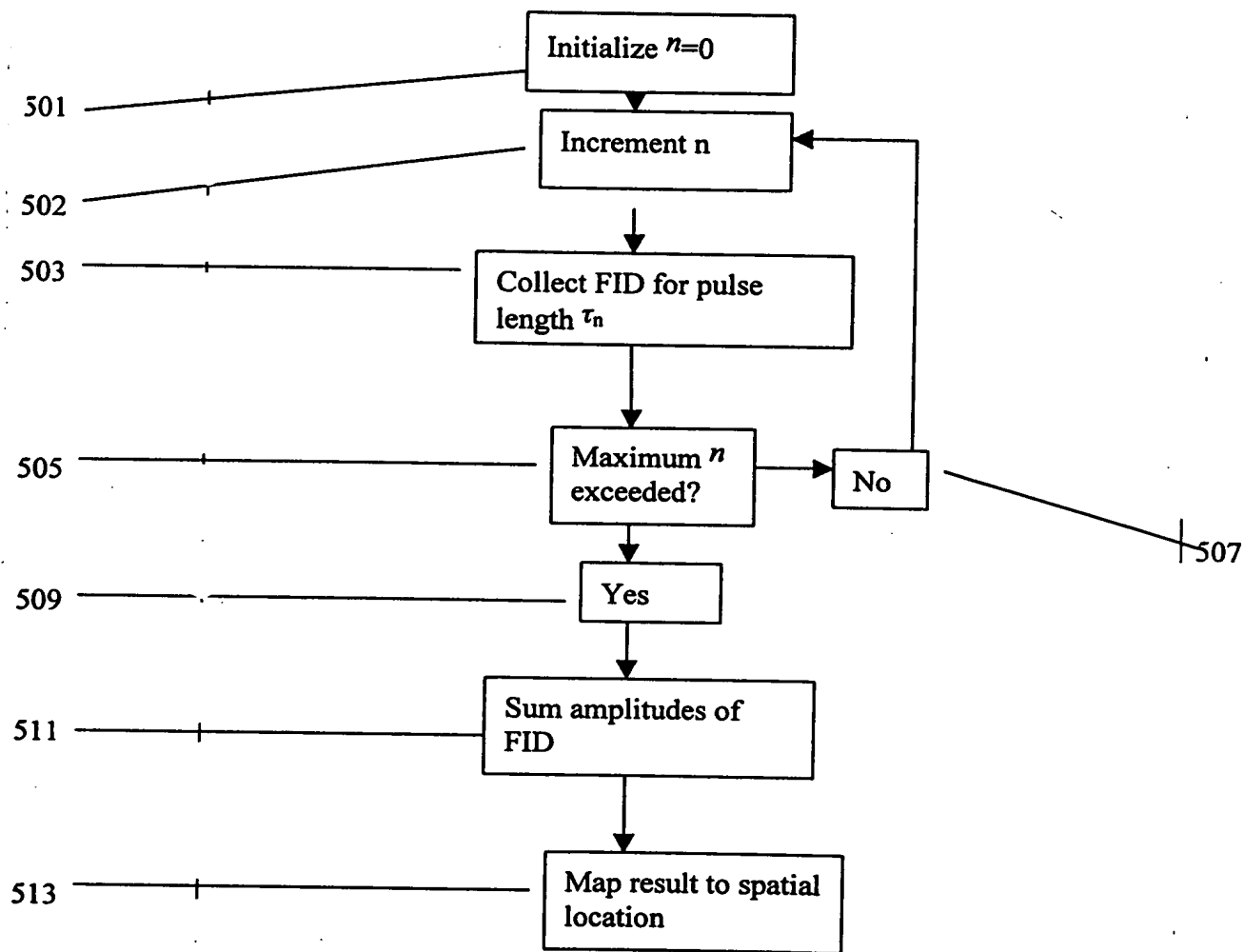


Figure 5

BEST AVAILABLE COPY

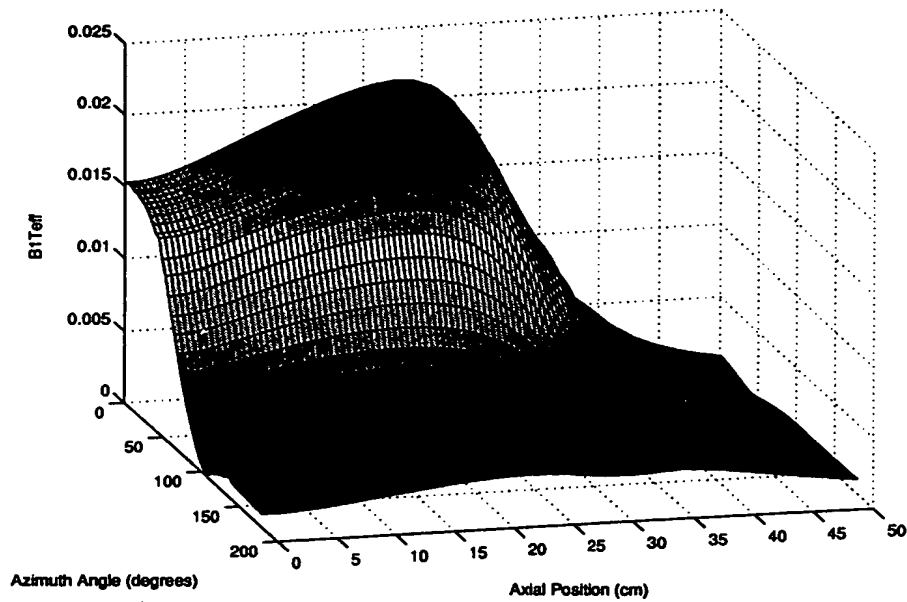


Figure 5a

Figure 5b

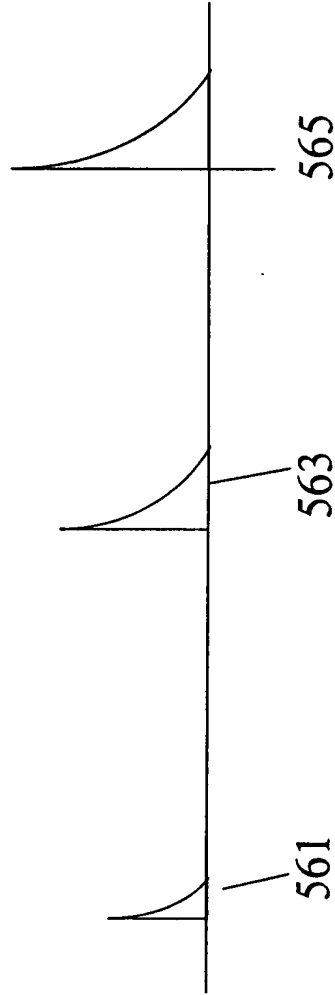
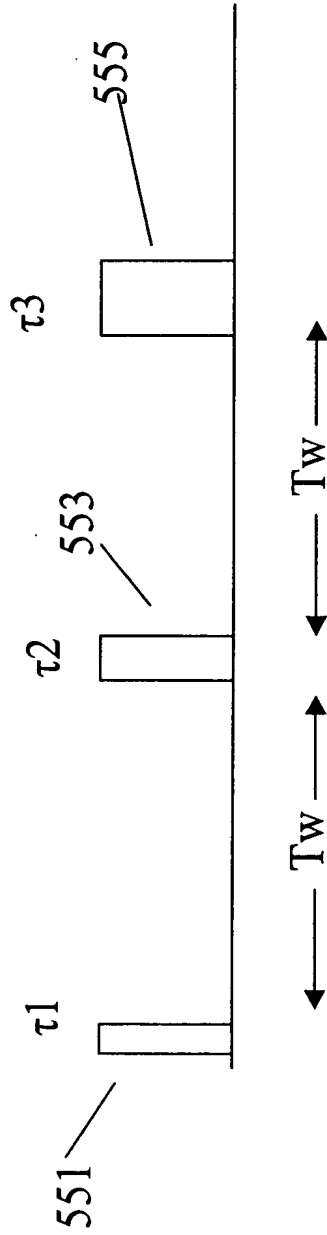


Figure 5c

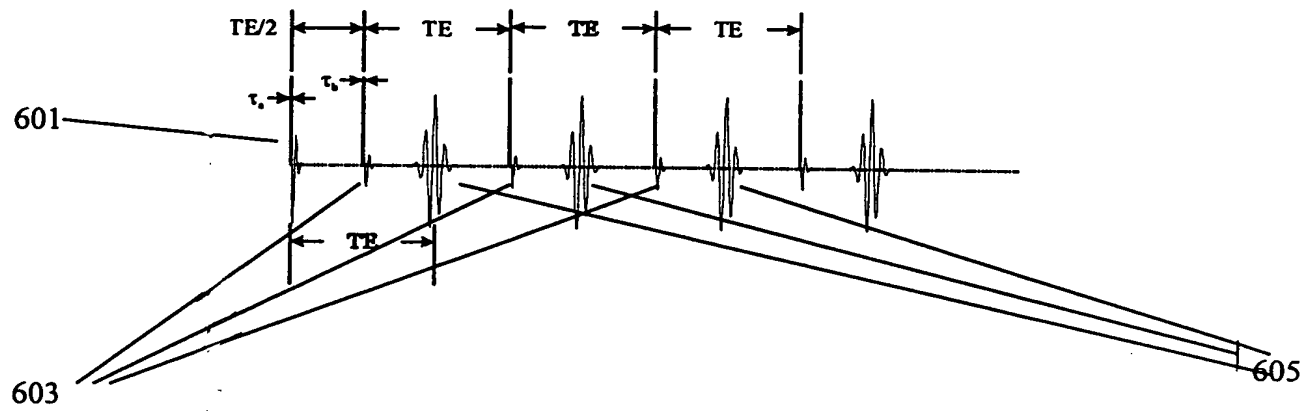
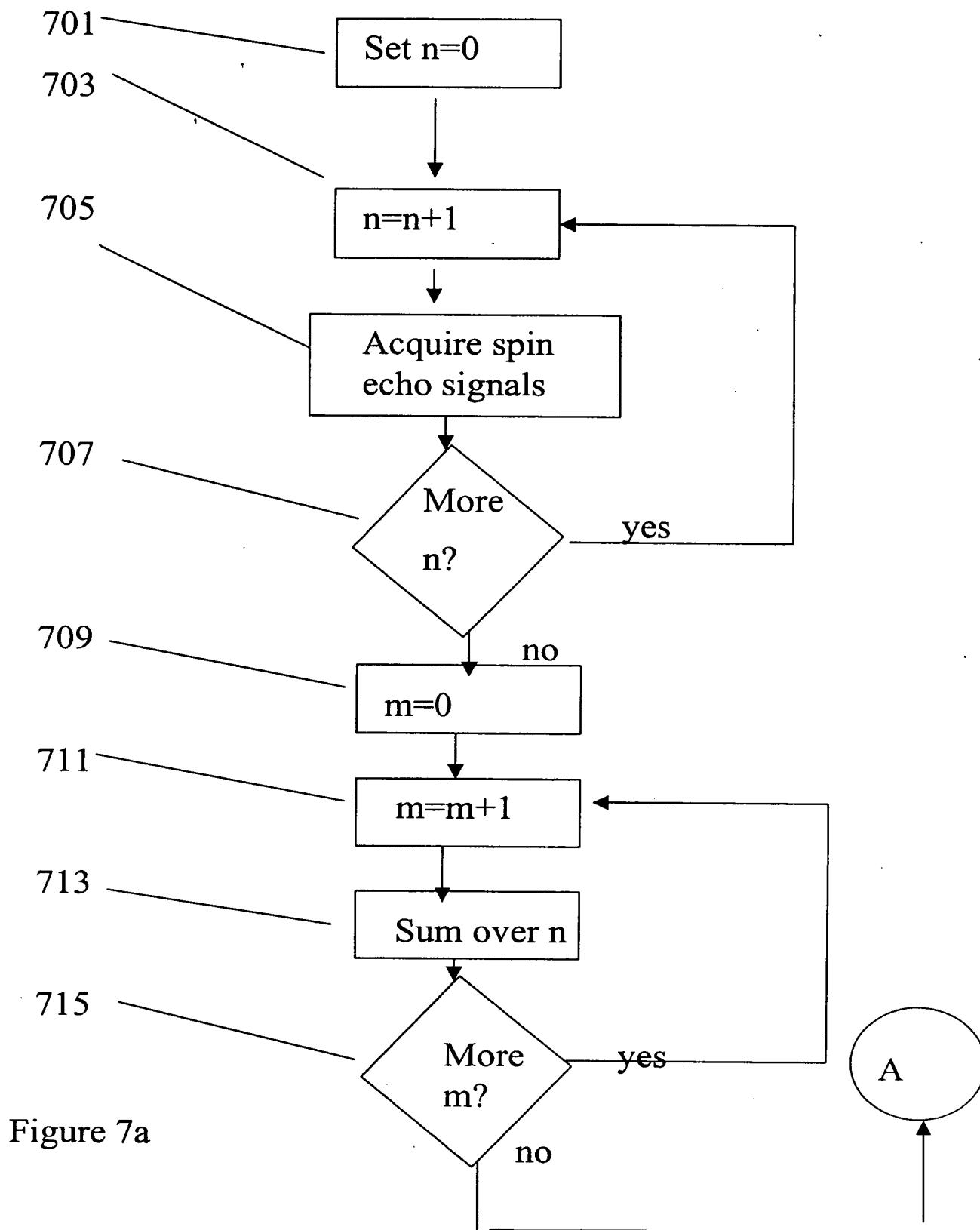


Figure 6



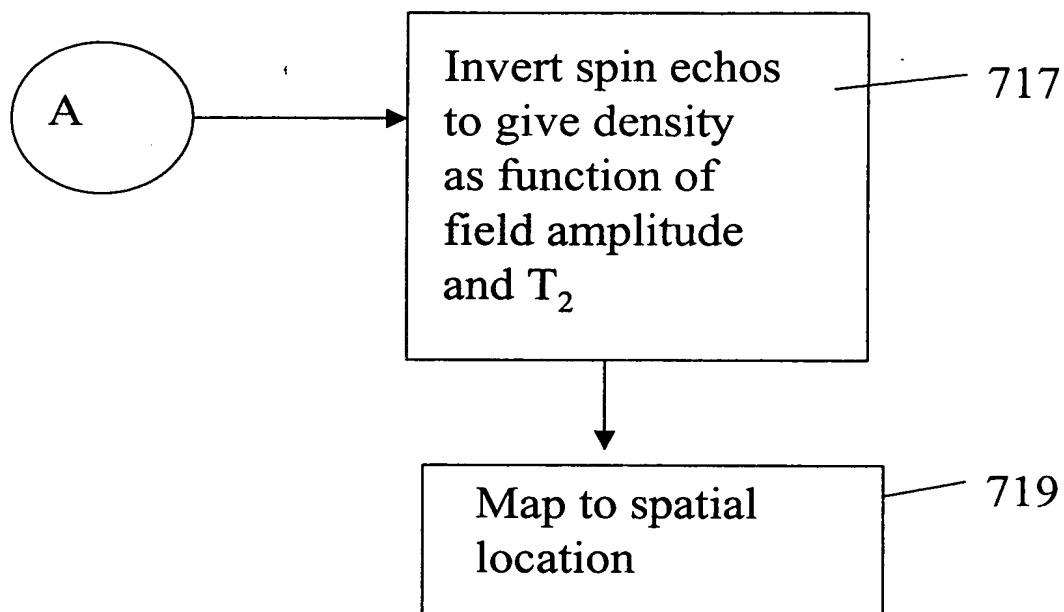


Figure 7b

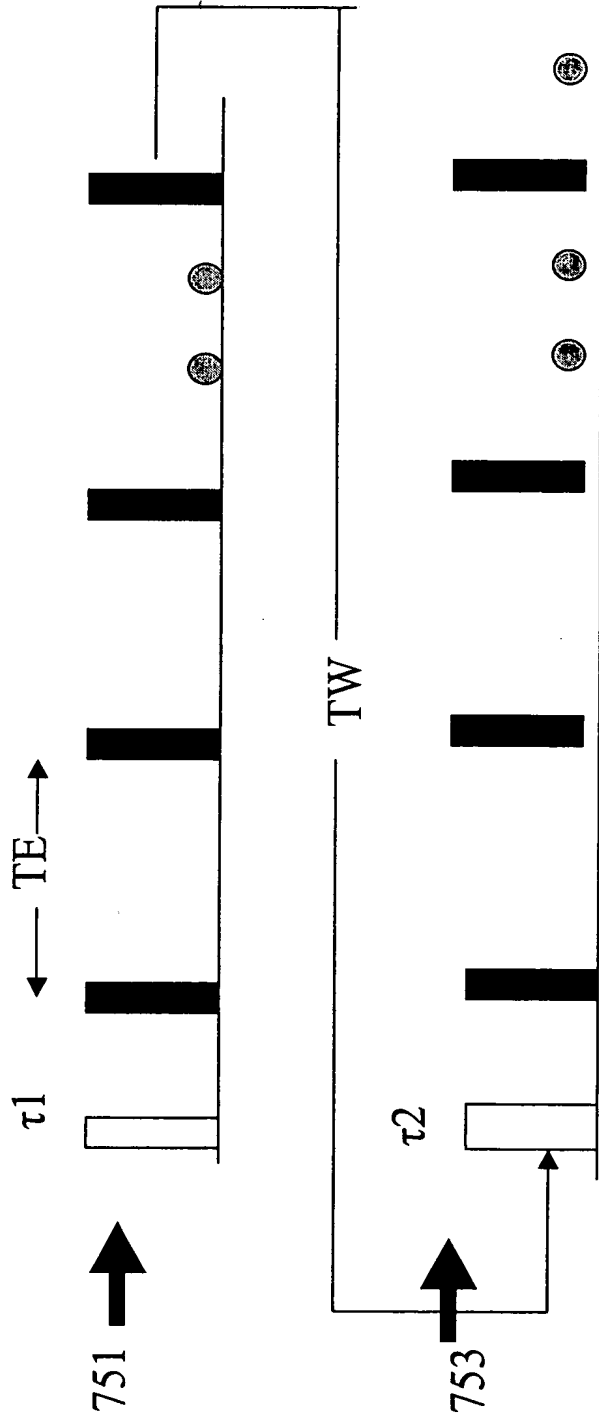


Figure 7c